

Notice of References Cited	Application/Control No. 10/631,062	Applicant(s)/Patent Under Reexamination HARRINGTON ET AL.	
	Examiner Richard Pantoliano Jr	Art Unit 2194	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,867,658	02-1999	Lee, Van Hoa	709/222
*	B	US-2004/0123086	06-2004	Rothman et al.	713/001
*	C	US-4,809,157	02-1989	Eilert et al.	718/104
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Itanium™ Processor Floating-point Software Assistance and Floating-point Exception Handling." Intel Corp., January 2000
	V	Jones, Steve. "Using spinlocks in a symmetric multiprocessing environment." Tech Specialist, v2, n10, pg 15(6), Oct 1991
	W	IBM Technical Bulletin: NNRD447149. "Method to Prevent Multiple Processes After Taking Exceptions To Enter Open Firmware In a Symmetrical Multiprocessor machine", Published 01 July 2001
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.